

FIG. 1

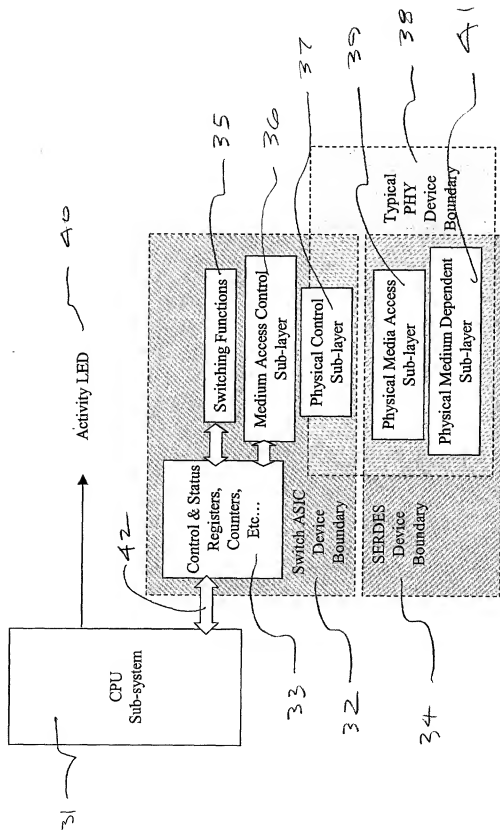


FIG. 2

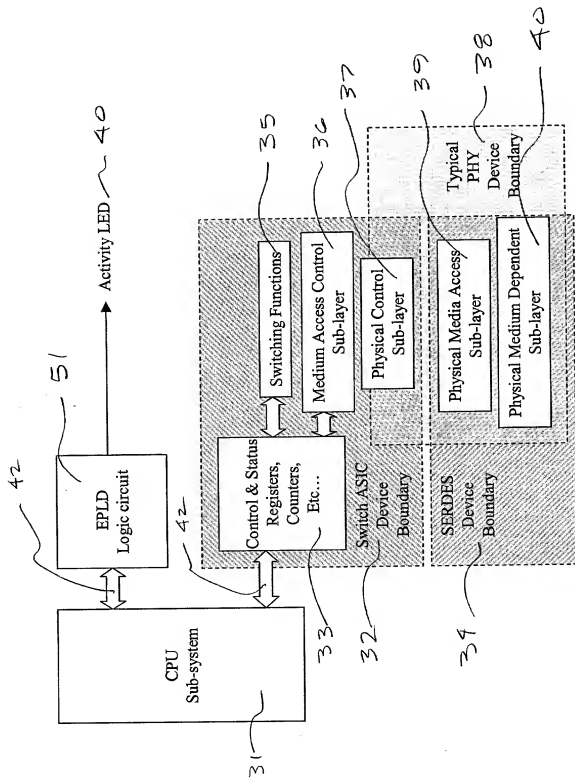


FIG. 3

51~

60

54

52

42

58

Logic
State
Machine

CPU Interface

Status
&
Control
Registers

Pseudo-Random
Number Generator

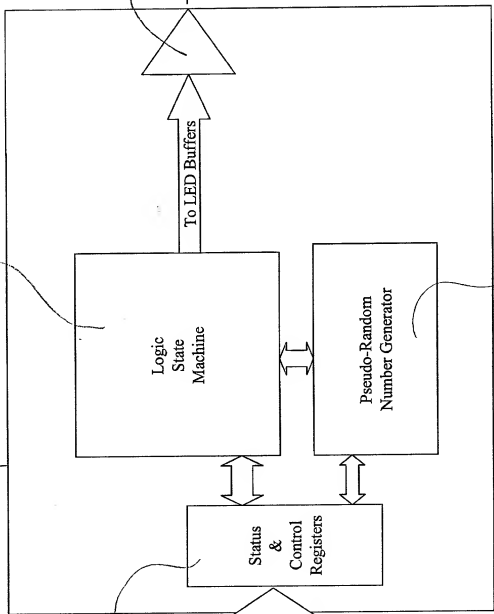
To LED Buffers

LED

~40

56

FIG. 4



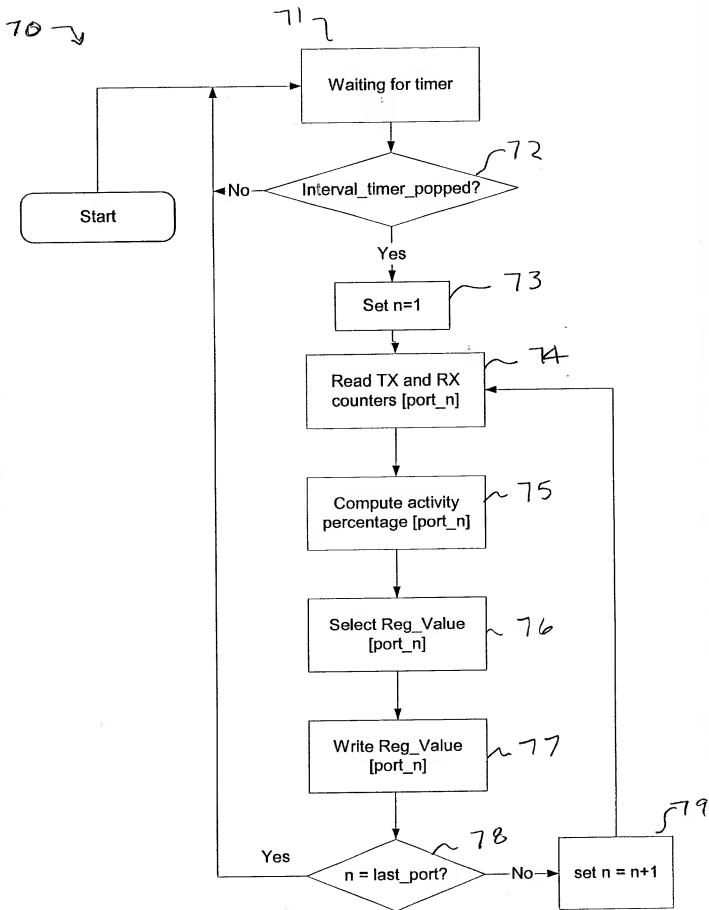


FIG. 5

10087412.030402

90 ↘

91 ↗

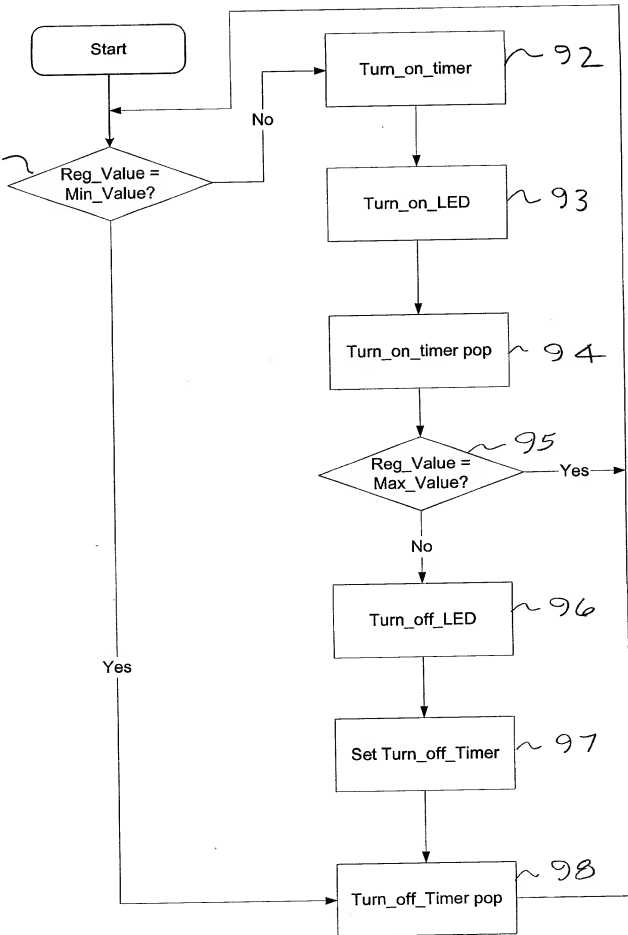


FIG. 6

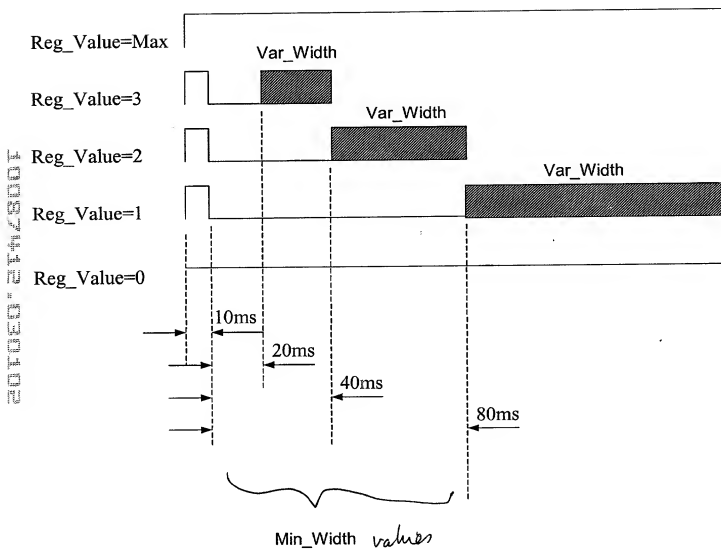


FIG. 7

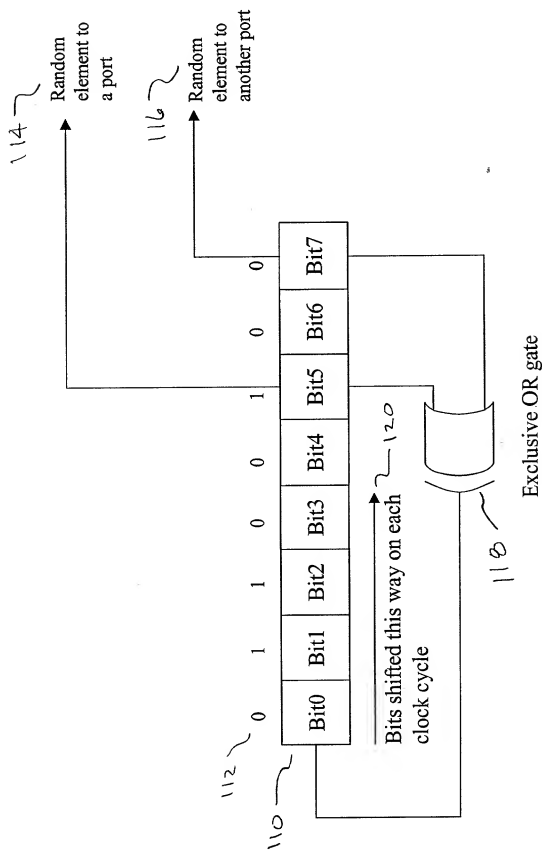


FIG. 8